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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/701,860

11/05/2003

Xu Zhu

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24122

7590

09/09/2004

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EXAMINER

ANYA, IGWE U

ART UNIT

PAPER NUMBER

2825

DATE MAILED: 09/09/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/701,860

Applicant(s)

ZHU ET AL.

Examiner

Igwe U. Anya

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 05 November 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-28 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 12-26 and 28 is/are allowed.
- 6) ☒ Claim(s) 1-11 and 27 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 05 November 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 6/21/04.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claim 1 is rejected under 35 U.S.C. 102(e) as being anticipated by De Pauw et al. (US Patent 6548327).

3. De Pauw et al. teach a process, comprising, reducing the thickness (col. 9 line 33 – col. 10 line 7) of a substrate carrying a plurality of devices, at least certain of the devices having a mesh (fig.8a), attaching a carrier wafer to the backside of the substrate (15), completing the fabrication of the devices from the topside of the substrate (fig 8b), singulating said plurality of devices (8c), and removing the carrier wafer (col. 10 lines 5 – 7).

4. Claims 1 – 3, 5, 7 – 11, and 27 are rejected under 35 U.S.C. 102(b) as being anticipated by Tai et al. (USPAP 2003/0199116).

5. Tai et al. teach a process, comprising, reducing the thickness of a substrate carrying a plurality of devices, at least certain of the devices having a mesh (fig. 4e), attaching a carrier wafer to the backside of the substrate (fig. 4f), completing the

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fabrication of the devices from the topside of the substrate (fig 4g), and singulating said plurality of devices (fig. 5). The completing comprises, forming and releasing at least certain of the meshes, and fabricating vent holes that connect through the substrate in at least certain of the areas where the released meshes are located, and singulating the devices is performed substantially simultaneously with the releasing (paragraph 46). Releasing at least certain of the formed meshes by removing a portion of substrate beneath the meshes by anisotropic etch to form pilot openings in the substrate using at least portions of certain of the formed meshes as an etch mask, and enlarging the pilot openings by isotropic etching to release the meshes and fabricate vent holes (fig. 4g).

Claim Rejections - 35 USC § 103

6. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

7. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to

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consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

8. Claims 4, and 6 are rejected under 35 U.S.C. 103(a) as being unpatentable over Tai et al. (USPAP 2003/0199116) in view of De Pauw et al. (US Patent 6548327).

9. Tai et al. teach the features previously outlined, but lack removing said carrier wafer, and wherein the fabricating is performed after the releasing.

10. However, De Pauw et al. teach removing the carrier wafer to singulate after dicing and fabrication (col. 10 lines 5 – 7).

11. Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to incorporate the teachings of De Pauw et al. into the Tai et al. reference to singulate chips after dicing and fabrication.

12. Claims 12 – 26, and 28 are allowable, because prior does not teach attaching carrier wafers on the front and backside of a substrate carrying devices having a mesh.

13. Prior considered, but not used in the rejection include Perdersen (USPAP 2003/0133588), Mullenborn et al. (US Patent 6732588), Gabriel et al. (USPAP 2004/0145056), Silverbrook (USPAP 2003/0210300), Gabriel et al. (UPAP 2003/0210799), and Wakabayashi et al. (US Patent 6603191).

14. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Igwe U. Anya whose telephone number is (571) 272-1887. The examiner can normally be reached on M - F 8:30am - 5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew S. Smith can be reached on (571) 272-1907. The fax phone

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number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Igwe U. Anya
Examiner
Art Unit 2825

IA

August 18, 2004


CARLOS E. RIVERA
PRIMARY EXAMINER